

FIG. 1

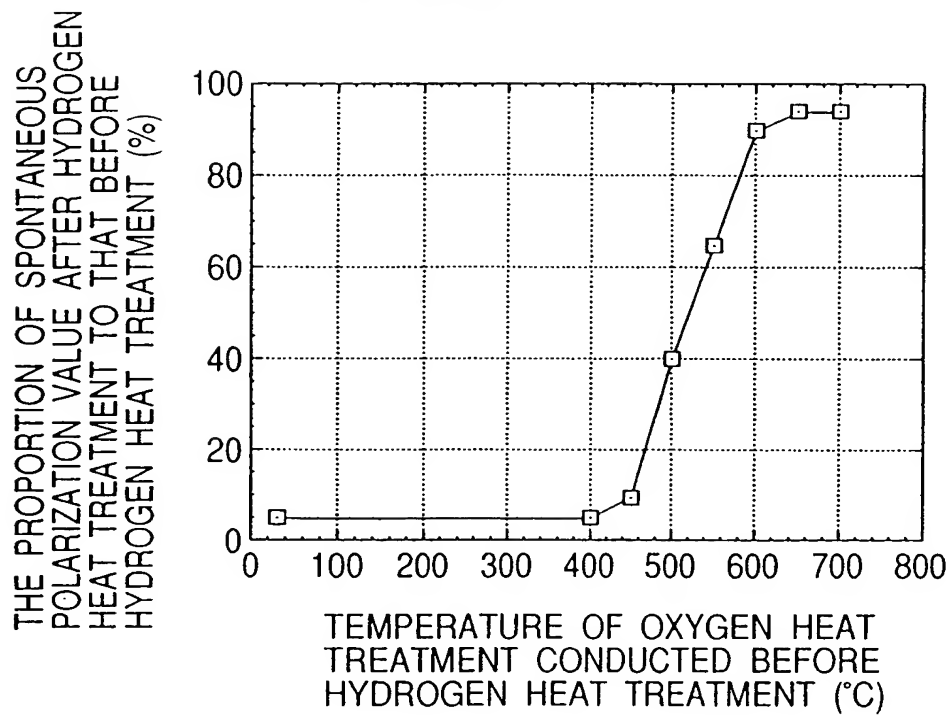


FIG. 2

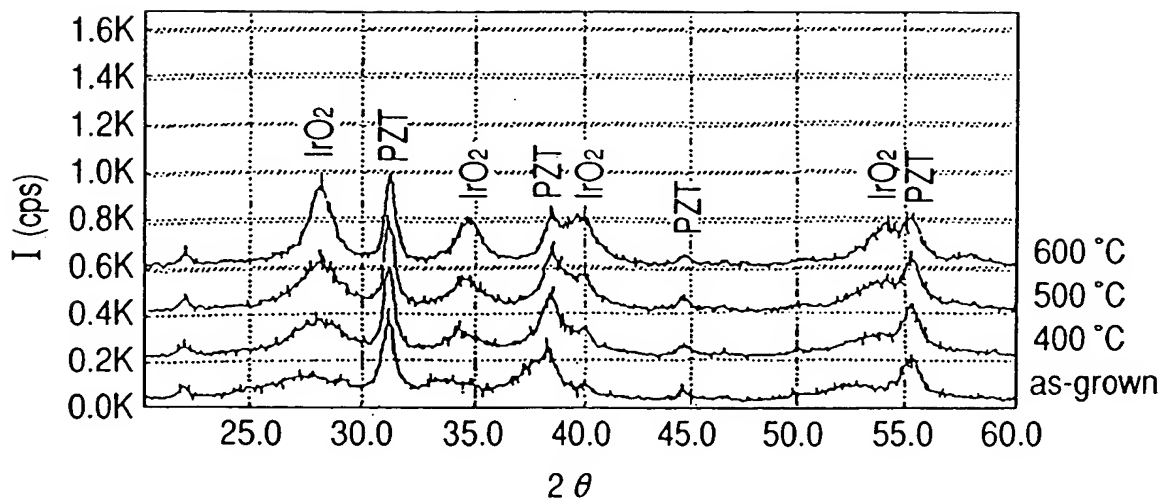


FIG. 3

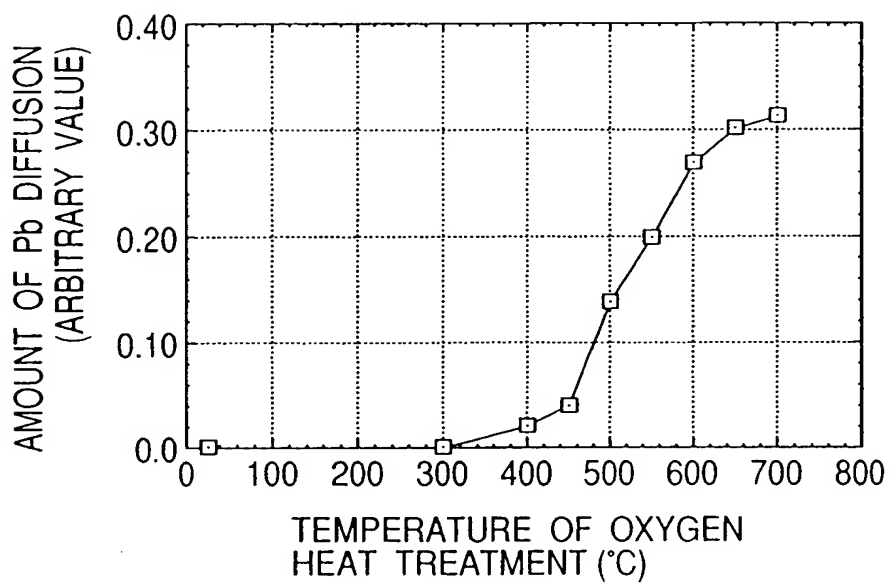


FIG. 4

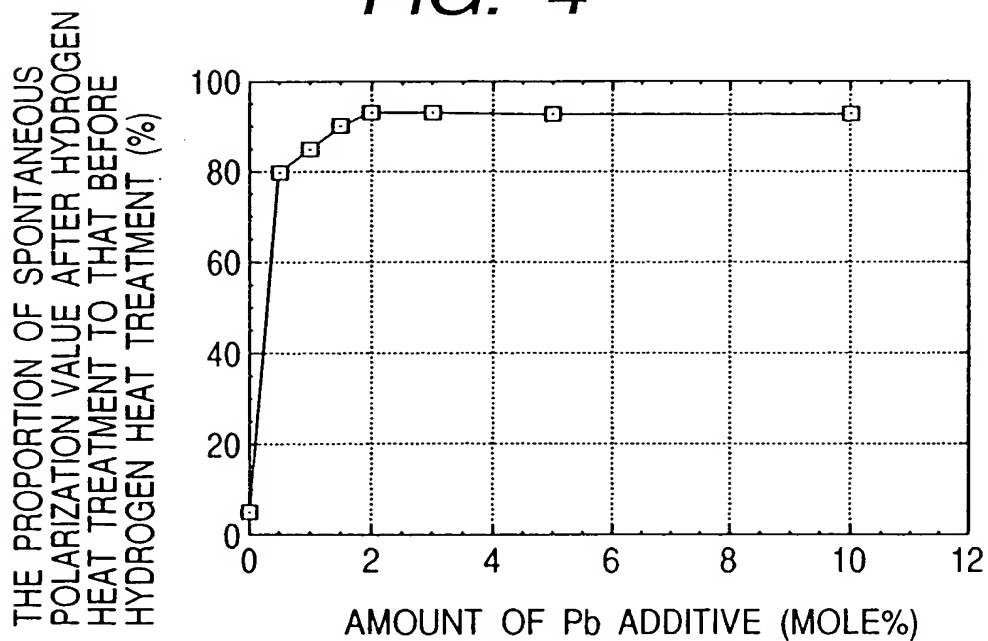


FIG. 5

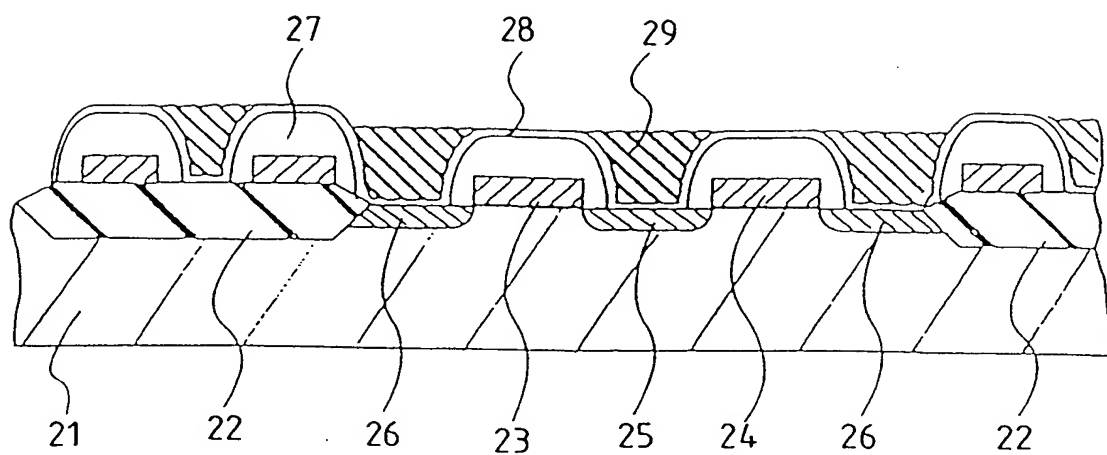


FIG. 6

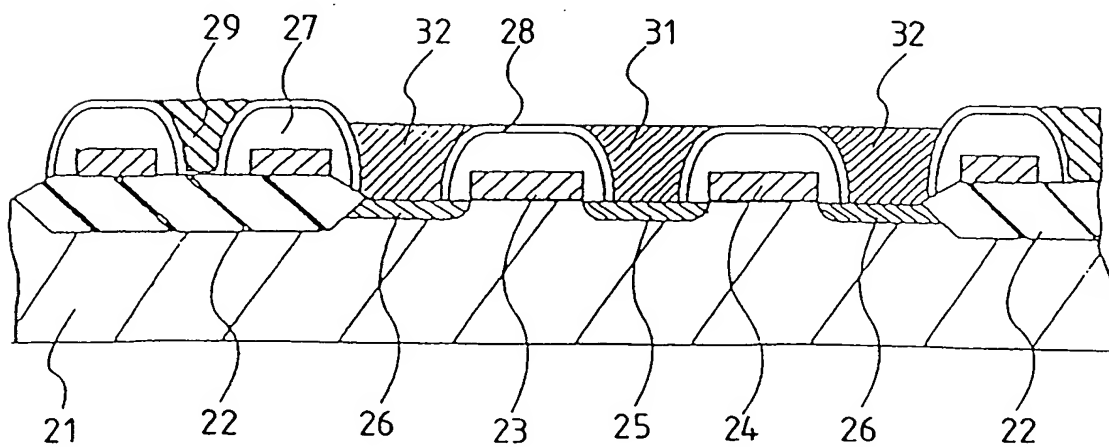


FIG. 7

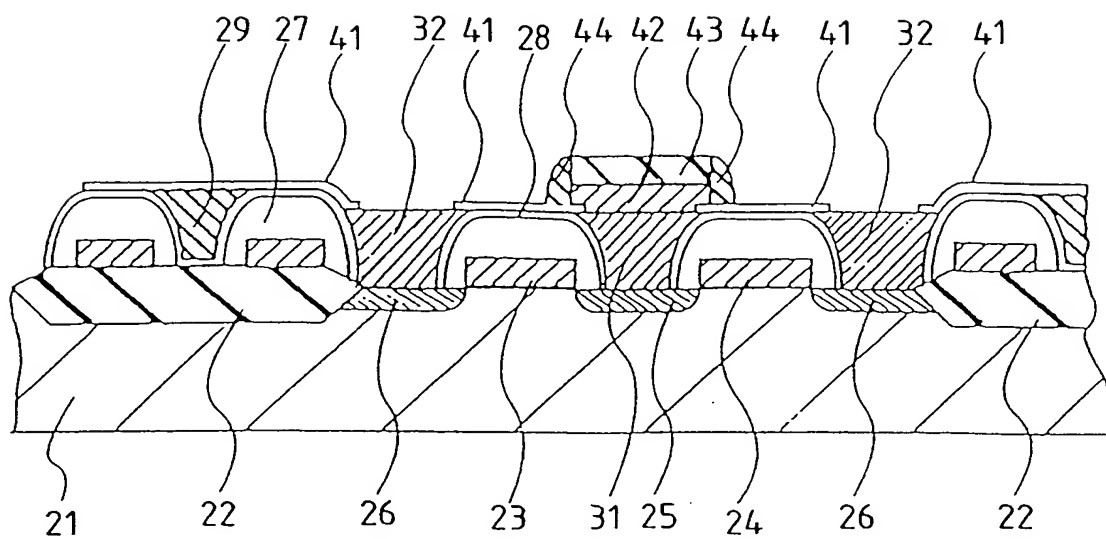


FIG. 8

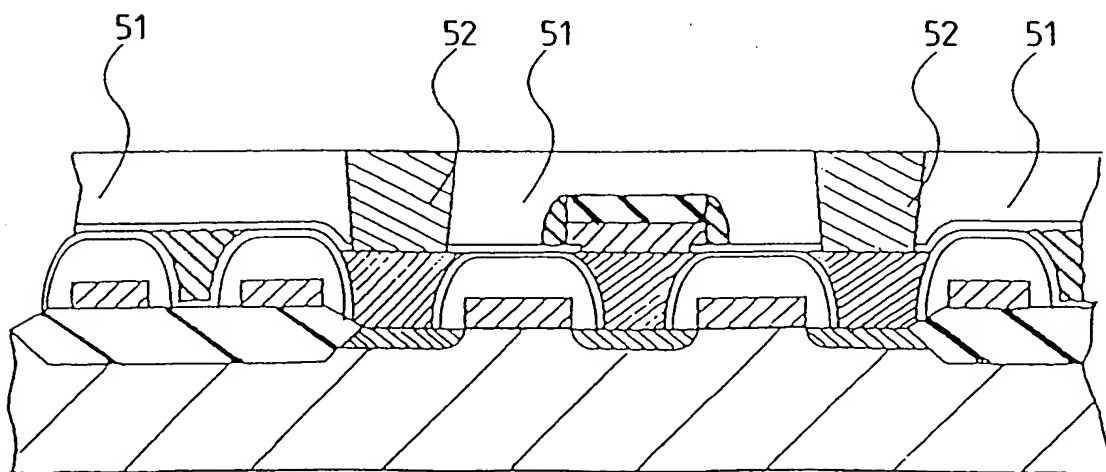


FIG. 9

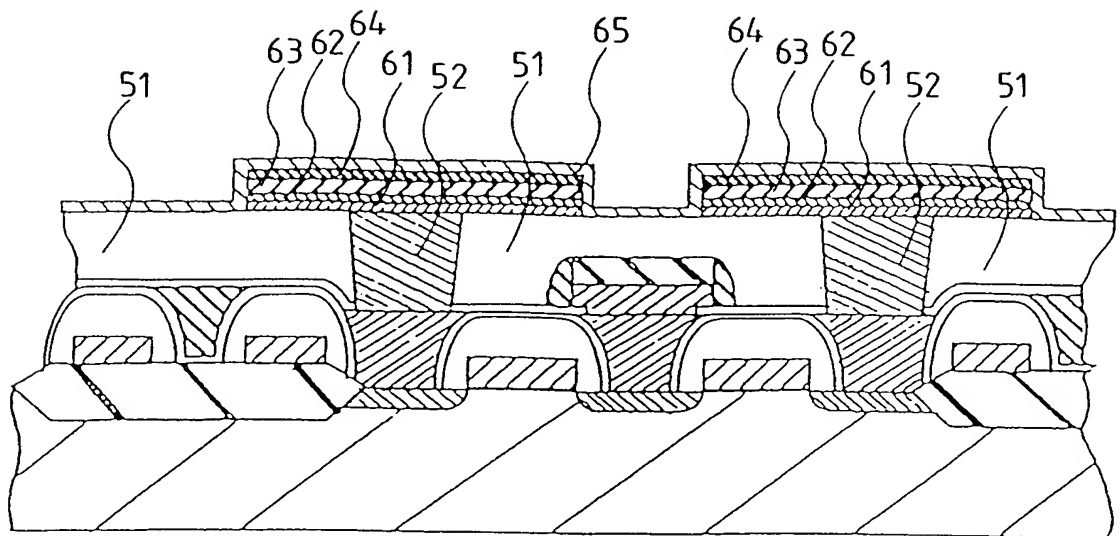
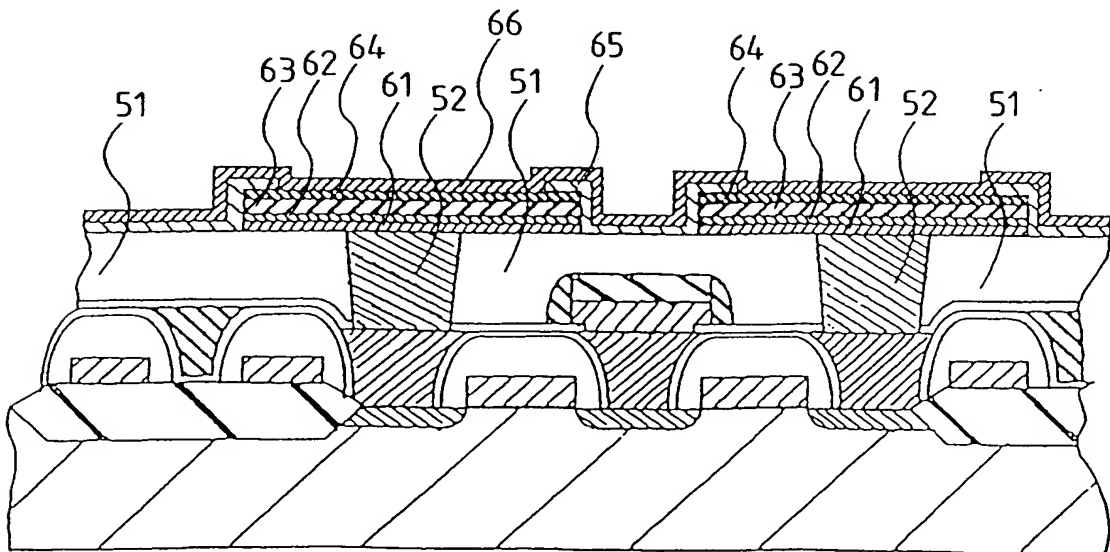


FIG. 10



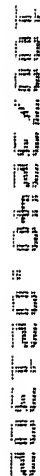
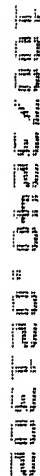
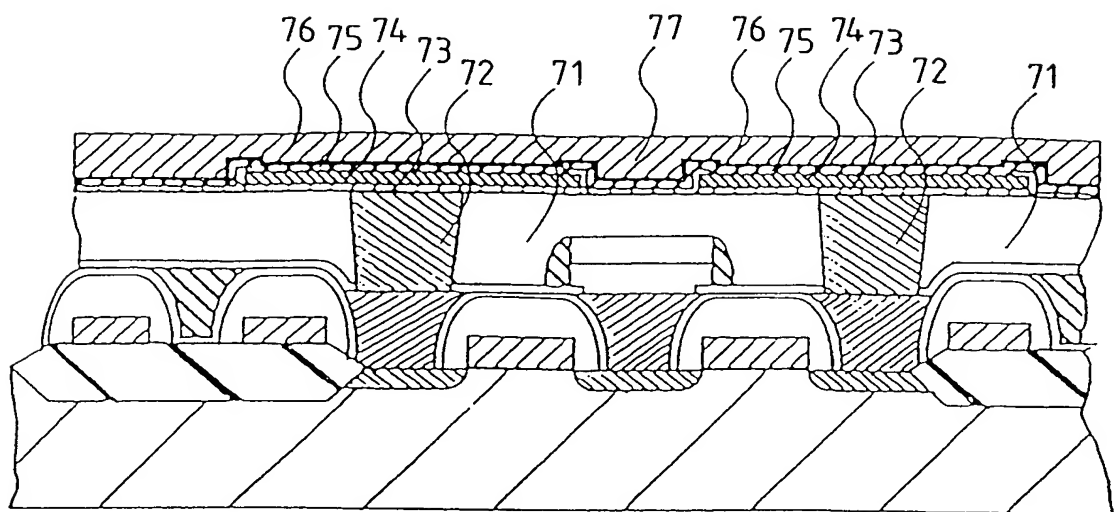
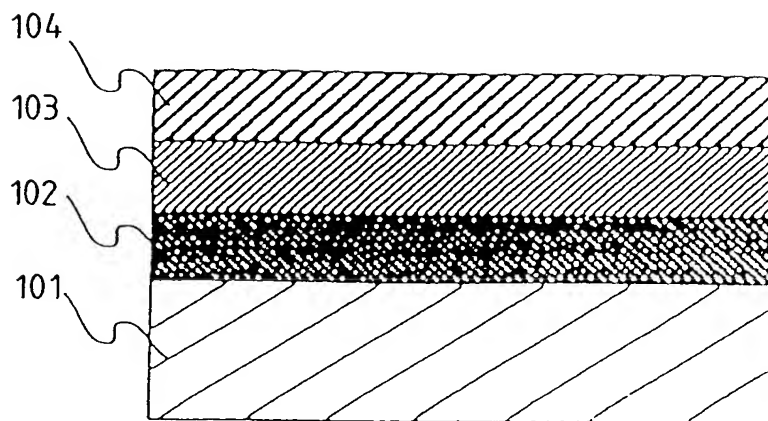
[illegible][illegible]

FIG. 13



*FIG. 14*



104: PLATINUM LAYER WITH Pb ADDITIVE  
(TOP ELECTRODE)

103: FERROELECTRIC THIN FILM

102: BOTTOM ELECTRODE

101: DEVICE LAYER INCLUDING TRANSISTORS  
(OR METAL SUPPORT)



FIG. 15

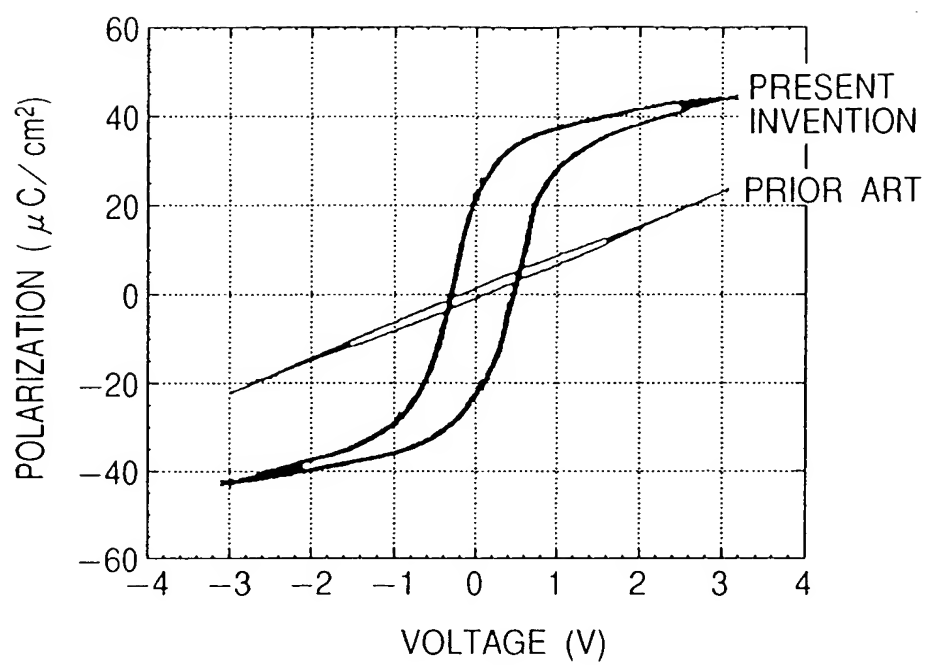


FIG. 16

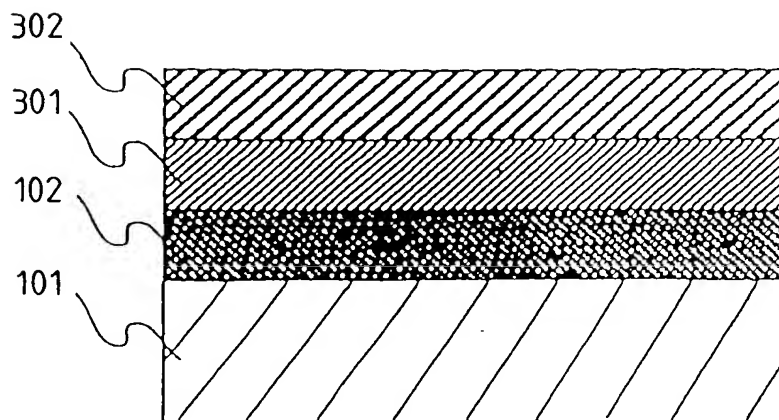
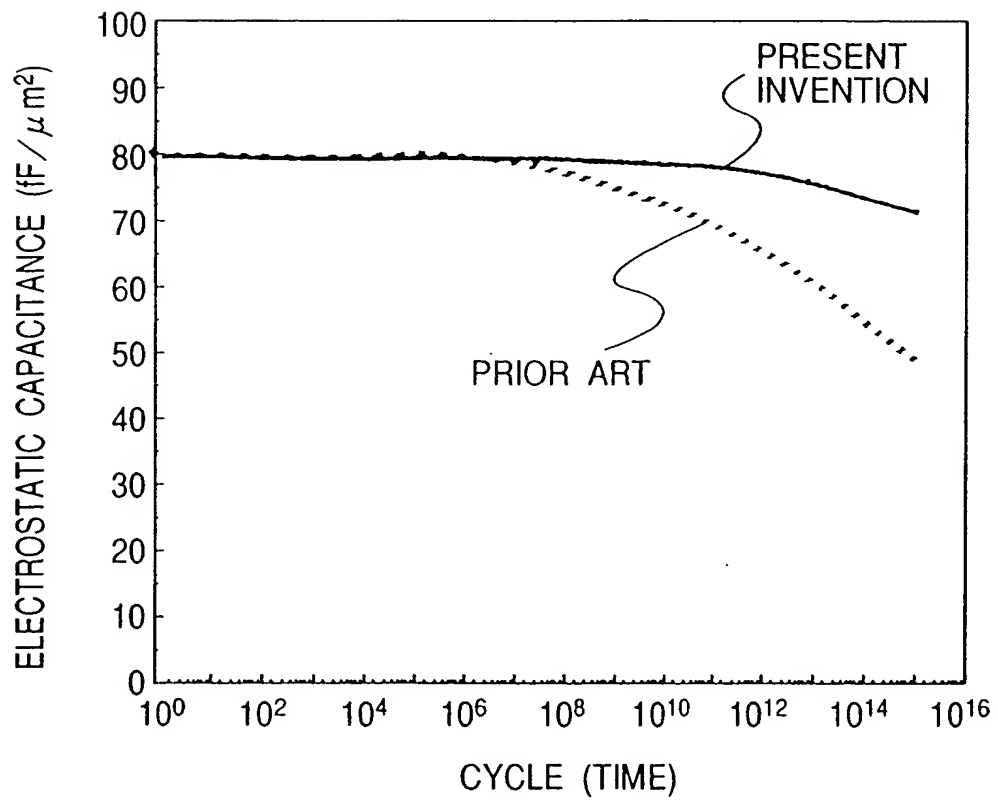
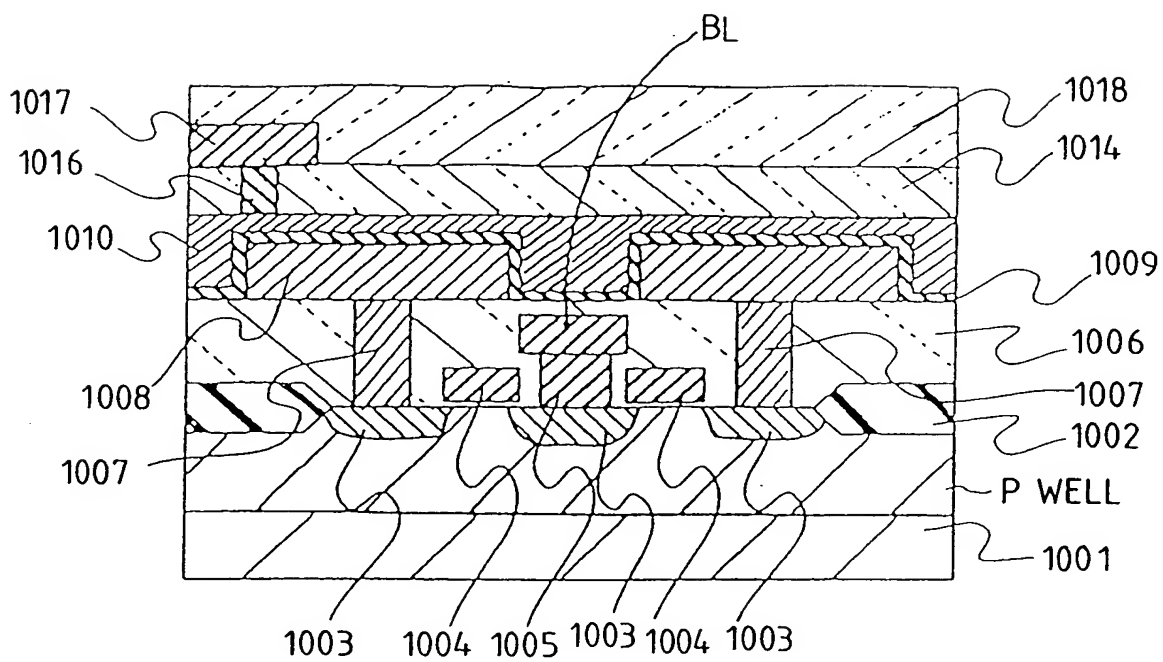


FIG. 17



1



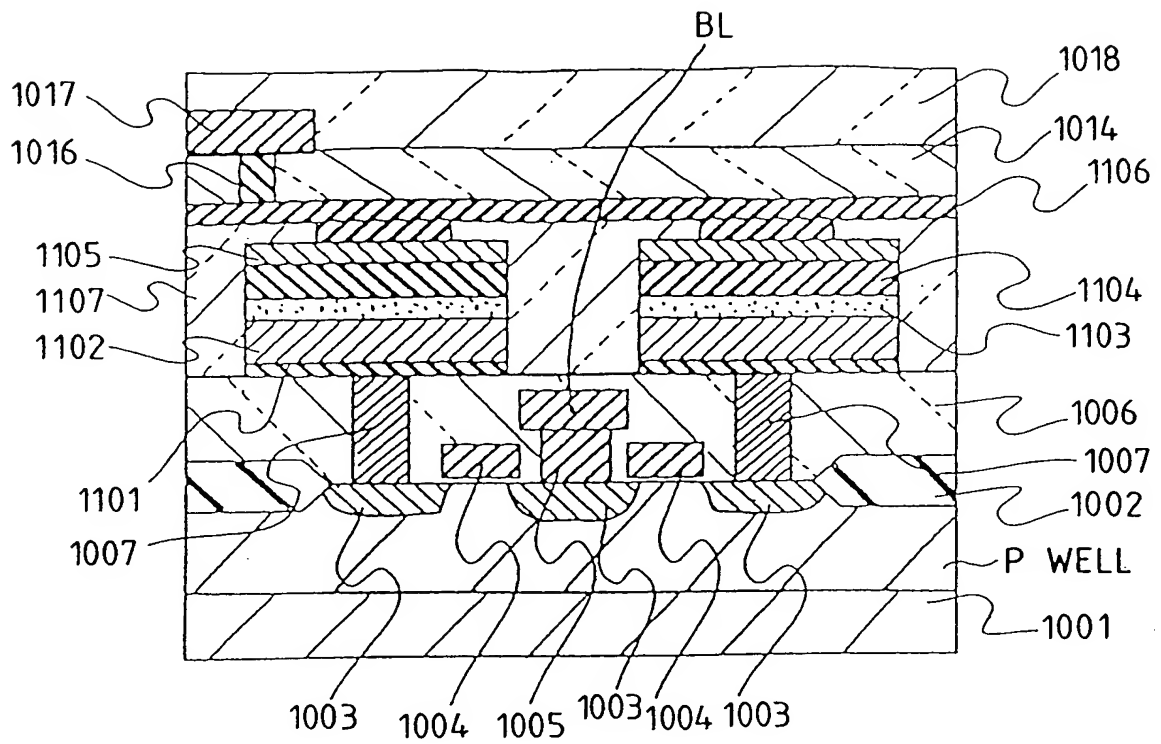
[illegible]

FIG. 20

